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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/736,618	12/17/2003	Yoshihiro Ohkura	X2007.0147	4069	
32172	7590 06/01/2006		EXAM	EXAMINER	
	SHAPIRO MORIN	NGUYEN, DILINH P			
1177 AVENU 41 ST FL.	1177 AVENUE OF THE AMERICAS (6TH AVENUE) 41 ST FI	ART UNIT	PAPER NUMBER		
	NY 10036-2714		2814		

DATE MAILED: 06/01/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Application No.	Applicant(s)		
		10/736,618	OHKURA, YOSHIHIRO	OHKURA, YOSHIHIRO	
	Office Action Summary	Examiner	Art Unit		
		DiLinh Nguyen	2814		
 Period for	The MAILING DATE of this communication appropriate Reply	opears on the cover sheet	with the correspondence address	,	
WHICH - Extensi after SI - If NO p - Failure Any rep	RTENED STATUTORY PERIOD FOR REPI HEVER IS LONGER, FROM THE MAILING I ions of time may be available under the provisions of 37 CFR 1 X (6) MONTHS from the mailing date of this communication. eriod for reply is specified above, the maximum statutory period to reply within the set or extended period for reply will, by statu- oly received by the Office later than three months after the mail- patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUN. .136(a). In no event, however, may d will apply and will expire SIX (6) Mile, cause the application to become	NICATION. a reply be timely filed ONTHS from the mailing date of this communicat ABANDONED (35 U.S.C. § 133).		
Status					
2a)	Responsive to communication(s) filed on <u>27.</u> This action is FINAL . 2b)⊠ The Since this application is in condition for allow closed in accordance with the practice under	is action is non-final. ance except for formal ma		is	
Dispositio	n of Claims				
5)	Claim(s) <u>1 and 3-9</u> is/are pending in the appl a) Of the above claim(s) is/are withdr Claim(s) is/are allowed. Claim(s) <u>1 and 3-9</u> is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and	awn from consideration.			
Applicatio	n Papers				
10)∐ T	he specification is objected to by the Examir he drawing(s) filed on is/are: a) ☐ ac	ccepted or b) objected t			
F	Applicant may not request that any objection to the Replacement drawing sheet(s) including the corrected to by the factors are to be the factors.	ection is required if the drawi	ng(s) is objected to. See 37 CFR 1.12		
Priority ur	nder 35 U.S.C. § 119				
12)⊠ A a)⊠ 1 2 3	cknowledgment is made of a claim for foreignal All b) Some * c) None of: Certified copies of the priority documents. Copies of the certified copies of the priority documents. Copies of the certified copies of the priority documents. Copies of the certified copies of the priority documents.	nts have been received. nts have been received in iority documents have bee au (PCT Rule 17.2(a)).	Application No en received in this National Stage		
Attachment(s) of References Cited (PTO-892)	4\ □ Intervies	w Summary (PTO-413)		
2) Notice 3) Information	of Draftsperson's Patent Drawing Review (PTO-948) ation Disclosure Statement(s) (PTO-1449 or PTO/SB/0 No(s)/Mail Date	Paper N	lo(s)/Mail Date of Informal Patent Application (PTO-152)		

U.S. Patent and Trademark Office PTOL-326 (Rev. 7-05) Art Unit: 2814

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.
- 2. Claim 1 is rejected under 35 U.S.C. 102(e) as being anticipated by Ohuchi et al. (U.S. Pat.6495916) (previously applied).

Ohuchi et al. disclose a surface mount chip package incorporating a semiconductor chip having an integrated circuit, comprising:

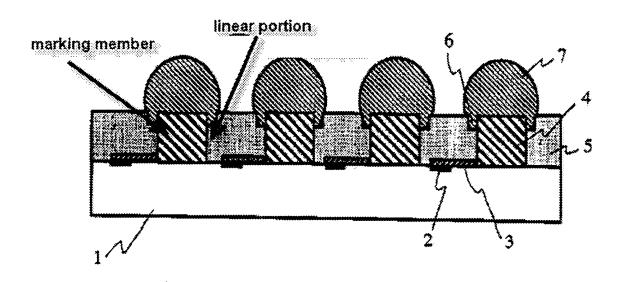
a package housing 5 made of a resin that covers the semiconductor chip 1 while avoiding a plurality of conductors 4 extending from the semiconductor chip 1;

a plurality of external electrodes 7 that are arranged in the package housing 5 in correspondence with a main surface of the semiconductor chip having the integrated circuit and are connected with the plurality of conductors extending from the semiconductor chip; the external electrodes 7 each having a circular shape when viewed in a vertical direction when the semiconductor chip is held horizontally; and

at least one marking member that is arranged in the package housing so as to realize a directivity when viewed in the vertical direction, wherein an outline shape of the marking member includes at least one linear portion as view from the vertical direction (fig. 1, column 2, lines 56 et seq.).

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Fig.1



Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claims 3-9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ohuchi et al. (U.S. Pat. 6495916) (previously applied) in view of AAPA (fig. 6) (previously applied).

- Regarding claim 3, Ohuchi et al. disclose that the marking member corresponds to a copper post (fig. 1, column 3, line 1) but do not explicitly disclose the marking member is arranged independently of the plurality of conductors. However, AAPA discloses that the marking member 520 is arranged independently of the plurality of conductors (fig. 6). Therefore, it would have been obvious to one having ordinary skill in the art to modify the device of Ohuchi et al. by having the marking member is arranged independently of the plurality of conductors, as taught by AAPA (fig. 6), in order to provide an index solder ball for use for the chip direction (page 2, lines 21-22).
- Regarding claim 4, Ohuchi et al. disclose that the copper post serving as the marking member has a square shape (fig. 1).
- Regarding claim 5, Ohuchi et al. disclose that the copper post is covered with a solder layer 7 (fig. 1, column 3, line 35).
- Regarding claim 6, Ohuchi et al. disclose that the marking member has a square shape (fig. 1).
- Regarding claims 7-9, Ohuchi et al. disclose that the marking member corresponds to a copper post (column 3, line 1) and it would have been obvious to increase in size compared with each of the plurality of conductors 4 (fig. 1).

Moreover, the semiconductor member is increased in size would have been obvious to an ordinary artisan practicing the invention because, absent evidence of disclosure of criticality for the size giving unexpected results, it is not inventive to discover optimal or workable ranges by routine experimentation. *In re Aller*, 220 F.2d

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454, 105 USPQ 233, 235 (CCPA 1955). Furthermore, the specification contains no disclosure of either the critical nature of the claimed dimensions of any unexpected results arising therefrom. Where patentability is aid to be based upon particular chosen dimensions or upon another variable recited in a claim, the Applicant must show that the chosen dimensions are critical. See *In re Woodruff*, 919 F.2d 1575, 1578, 16 USPQ2d 1934, 1936 (Fed, Cir. 1990).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DiLinh Nguyen whose telephone number is (571) 272-1712. The examiner can normally be reached on 8:00AM - 6:00PM (M-F).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571) 272-1705. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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